



- ☐ Pending
- ☒ Active
 - ☒ L3: (893) (inverter same (startS3 or ignitS3)) and switch
 - ☒ L4: (867) 3 and voltage
 - ☒ L5: (669) 4 and transistor
 - ☒ L6: (399) 5 and AC and DC
 - ☒ L7: (71) 6 and diac
 - ☒ L8: (71) 7 and capacitor
 - ☒ L9: (71) 8 and (315/224 or 315/209T or 315/DIG. 7
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☐ Trash

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DBs US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

Default operator: OR

☒ Plurals☒ Highlight all hit terms initially

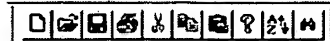
BRS form IS&R form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Er	Error
1	BRS	L3	893	(inverter same (startS3 or ignitS3)) and switchS3 and resonanS2 and (delayS3 same (startS3 or triggerS3 or ignitiS3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:33			
2	BRS	L4	867	3 and voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:34			
3	BRS	L5	669	4 and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:34			
4	BRS	L6	399	5 and AC and DC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:35			
5	BRS	L7	71	6 and diac	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:35			
6	BRS	L8	71	7 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:35			
7	BRS	L9	71	8 and (315/224 or 315/209T or 315/DIG. 7 or 307/140 or 307/141)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/09/01 12:37			

Hits Details HTML

Ready

NUM



- ☐ Drafts
- ☐ Pending
- ☒ Active
 - ☒ L1: (1) ((inverter with (ignitS3 or startS3)) and switchS3 and (alternating adj current)) and resonant and (voltage near3 start-up) and (delayS3 same triggerS3)).csm.
 - ☒ L2: (1) ((convertS3 with AC with DC) and capacitor and diac and (bus adj voltage) and lamp).csm.
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☐ Trash

Search List Browse Queue Clear

DBs US-PGPUB

☒ Plurals☒ Highlight all hit terms initially

Default operator: OR

BRS form IS&R form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Er	Error
1	BRS	L1	1	((inverter with (ignitS3 or startS3)) and switchS3 and (AC or (alternating adj current)) and resonant and (voltage near3 start-up) and (delayS3 same triggerS3)).csm.	US-PGPUB	2005/09/01 12:51			
2	BRS	L2	1	((convertS3 with AC with DC) and capacitor and diac and (bus adj voltage) and lamp).csm.	US-PGPUB	2005/09/01 12:53			

Hits Details HTML

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RUN